



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

YOUFENG WU

Application No.: 09/965,757

Filed: September 28, 2001

For: **METHOD, APPARATUS, AND SYSTEM  
TO FORMULATE REGIONS OF  
REUSABLE INSTRUCTIONS**

Art Group: 2122

Examiner: Unassigned

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**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 CFR § 1.56 and in accordance with 37 C.F.R. § 1.97, enclosed is a copy of Information Disclosure Citation Form PTO/SB/08. Copies of the references cited on PTO/SB/08 are enclosed herewith. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).


Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any additional fee due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: March 07, 2002



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Laura R. Robles

03/07/02  
Date

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**CROSS-REFERENCE UNDER 37 C.F.R. §1.78(a)(2) TO RELATED APPLICATIONS**

Sir/Madam:

Pursuant to 37 C.F.R. §1.78(a)(2), applicants hereby cross-reference the following related patent applications, which are all assigned to the same assignee as the present patent application.

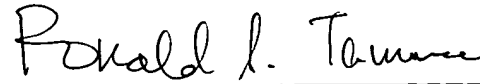
(1) A U.S. Patent Application entitled Method and Apparatus for Performing Compiler Transformation of Software Code Using Fastforward Regions Value Specification by Youfeng Wu, et al., U.S. Application No. 09/969,962, filed September 29, 2001, attorney docket reference No. 042390.P11903.

(2) A U.S. Patent Application entitled Method, Apparatus, and System to Optimize Frequently Executed Code And To Use Compiler Transformation And Hardware Support To Handle by Youfeng Wu, et al., U.S. Patent Application No. 10/017,669, filed October 30, 2001, attorney docket reference No. 042390.P10350.

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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



RONALD S. TAMURA

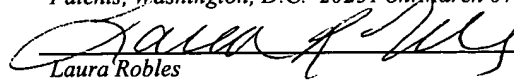
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Laura Robles

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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>		
				Application Number	09/965,757	
Sheet		2	of	2	Filing Date	September 28, 2001
					First Named Inventor	Youfeng Wu
					Group Art Unit	2122
					Examiner Name	Unassigned
					Attorney Docket Number	42390P10792

OTHER ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	C	PATH PROFILE GUIDED PARTIAL DEAD CODE ELIMINATION USING PREDICATION, by RAJIV GUPTA, et al., PACT '97, November 11-15 1997 in San Francisco, California	
	D	COMPILER-DIRECTED DYNAMIC COMPUTATION REUSE: RATIONALE AND INITIAL RESULTS, by DANIEL A. CONNORS, et al., Department of Electrical and Computer Engineering Center for Reliable and High-Performance Computing, University of Illinois, Urbana, IL 61801, Nov. 1999	
	E	IMPROVING TRACE CACHE EFFECTIVENESS WITH BRANCH PROMOTION AND TRACE PACKING, by SANJAY JERAM PATEL, et al., Advanced Computer Architecture Laboratory, Department of Electrical Engineering and Computer Science, The University of Michigan, Ann Arbor, Michigan 48109-2122	
	F	THE SUPERBLOCK: AN EFFECTIVE TECHNIQUE FOR VLIW AND SUPERSCALAR COMPILATION, by WEN-MEI W. HWU, et al., Center for Reliable and High-Performance Computing, University of Illinois, Urbana-Champaign, Illinois, 61801.	
	G	REPLAY: A HARDWARE FRAMEWORK FOR DYNAMIC PROGRAM OPTIMIZATION, by SANJAY J. PATEL, et al., Center for Reliable and High-Performance Computing, The University of Illinois at Urbana-Champaign, CRHC Technical Report # CRHC-99-16, December 1999	

Examiner Signature	Date Considered
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**Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication

<sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

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